

**Notice of References Cited**Application/Control No.  
09/786,979Applicant(s)/Patent Under  
Reexamination  
HERMANN ET AL.Examiner  
Lynne EdmondsonArt Unit  
1725

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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-4,578,554	03-1986	Coulter, Leland E.	219/121.63
	B	US-5,592,294	01-1997	Ota et al.	192/85R
	C	US-5,961,858	10-1999	Britnell, James W.	219/121.63
	D	US-4,855,565	08-1989	Thomas et al.	219/121.74
	E	US-5,347,454	09-1994	Mushabac, David R.	433/214
	F	US-5,293,023	03-1994	Haruta et al.	219/121.6
	G	US-4,618,758	10-1986	Gilli et al.	219/121.67
	H	US-4,324,972	04-1982	Furrer et al.	219/121.63
	I	US-5,446,549	08-1995	Mazumder et al.	356/613
	J	US-6,316,743	11-2001	Nagahori et al.	219/121.67
	K	US-5,304,773	04-1994	Kilian et al.	219/121.78
	L	US-5,515,599	05-1996	Best, Norman D.	29/38C
	M	US-6,392,192	05-2002	Cole et al.	219/121.62

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	JP-10-128571-A	05-1998	Japan	Nakamura	
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Interlocking Anti-Crash and Conveyor Indexing Control Card for High-Speed Substrate Testers", IBM Technical Disclosure Bulletin, July 1990, Vol. 33, Issue 2, Pages 31-34. IBM TDB-ACC-NO: NN600731
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.